



Docket No.: 57454-257

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES

In re Application of

Fukashi MORISHITA

Serial No.: 09/987,566

Filed: November 15, 2001

Group Art Unit: 2816

Examiner: Terry D. Cunningham

For: INTERNAL POWER SUPPLY VOLTAGE GENERATION CIRCUIT THAT CAN
SUPPRESS REDUCTION IN INTERNAL POWER SUPPLY VOLTAGE IN
NEIGHBORHOOD OF LOWER LIMIT REGION OF EXTERNAL POWER SUPPLY
VOLTAGE

NOTICE OF APPEAL
FROM THE PRIMARY EXAMINER
TO THE BOARD OF APPEALS

Commissioner for Patents
Washington, DC 20231

Sir:

Applicant(s) hereby appeal(s) to the Board of Appeals from the decision dated July 17, 2002
of the Primary Examiner finally rejecting claims 19, 20 and 22.

Appeal Fee: \$320.00

- ☐ Not required (fee paid in prior appeal in this application).
- ☒ Charge to Deposit Account No. 500417.
- ☐ Check Enclosed.

11/15/2002 SLUAN61 00000045 500417 09987566
02 FC:1401 320.00 CH

#10/Notice of
Appeal
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TECHNOLOGY CENTER 2800
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Dm H

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

MCDERMOTT, WILL & EMERY



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Date: November 14, 2002